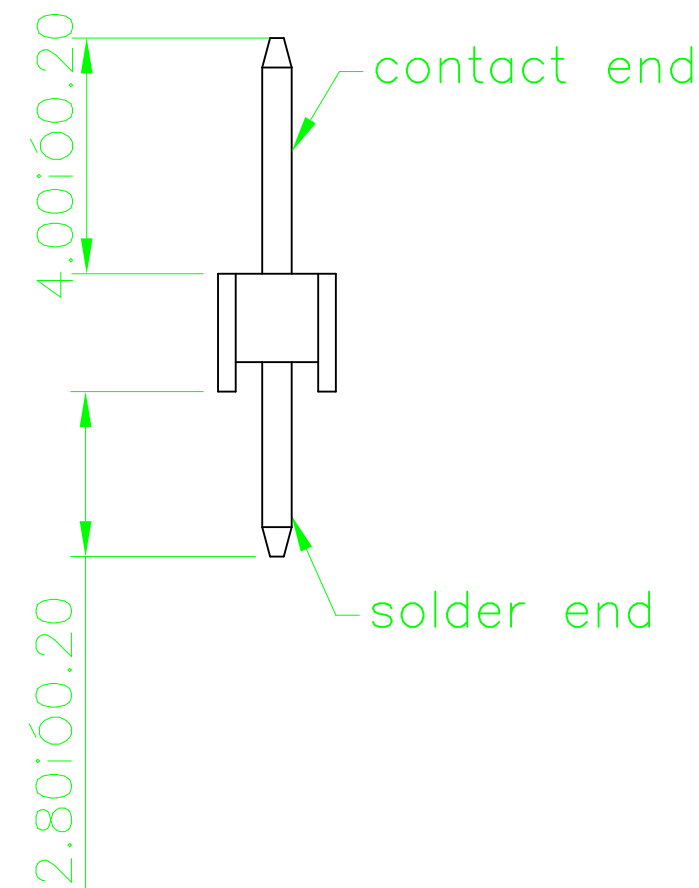
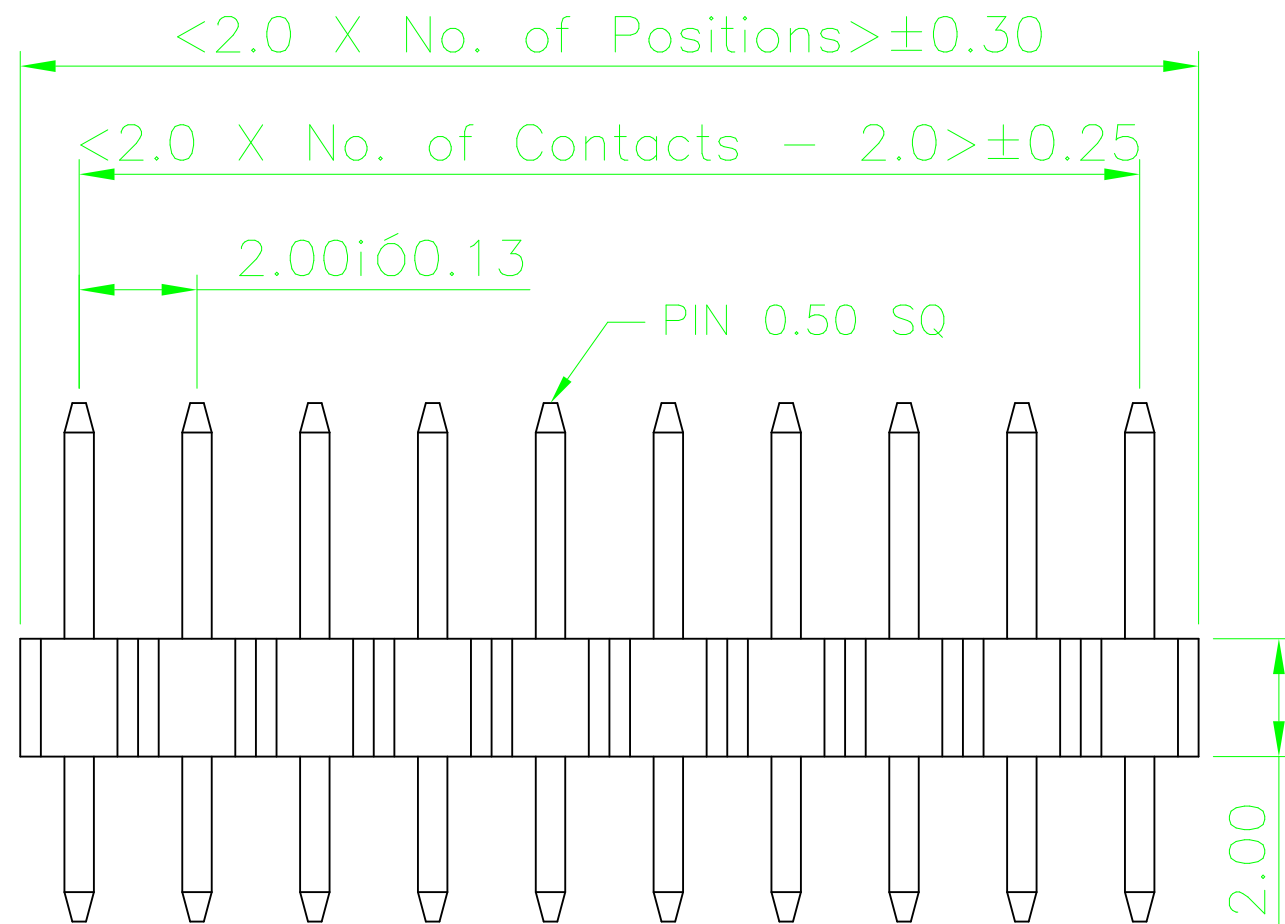
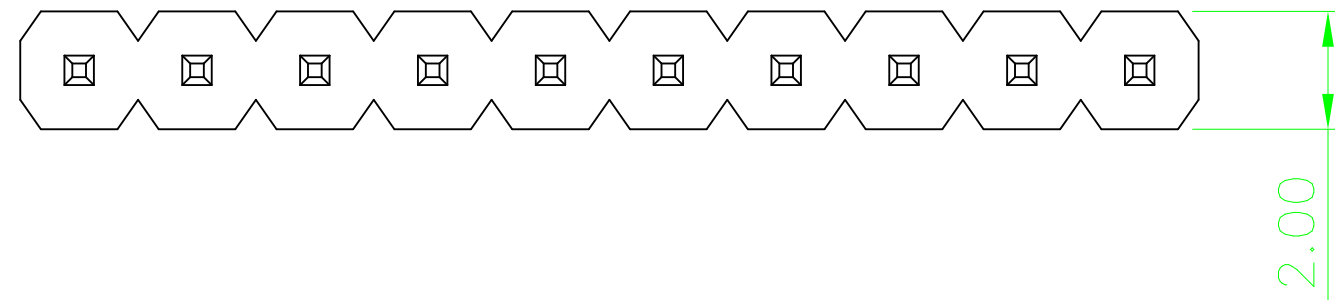


REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
01		INITIAL RELEASE.	2010-06-14	Lengjianjun
02	△01	Change the No. of Pins per Row 02~40 → 01~40	2010-06-14	CHENG



Specifications

Current Rating: 2Amp

Insulation Resistance: 1000MΩ Min.

Dielectric Withstanding: 500V AC/DC

Contact Resistance: 20mΩ Max.

Operating Temperature: -40°C to +105°C

Materials

Contact Material: Brass

Insulation Material: Polyester, UL 94V-0

PBT

All materials must be Geron materials

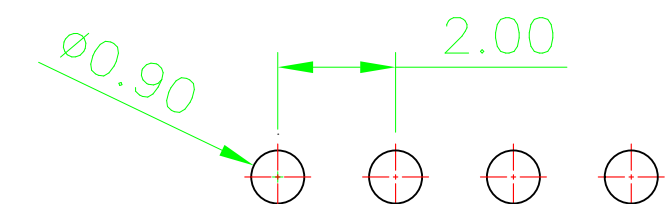
Ordering Information

GRA71 - XX XX Z002

No. of Pins per Row
△01 01~40

Contact Plating
01=Tin
02=Gold Flash
Duplex Plating
05=Gold Flash/Tin
...

Recommended P.C.Board
Hole Layout
(Tolerance: ±0.05)



GERON ELECTRONIC CO., LTD

GENERAL TOLERANCE

X.° ± 5° X. ± 0.30

.X° ± 2° .X ± 0.20

.XX° ± 1° .XX ± 0.15

.XXX° ± 0.5° .XXX ± 0.10

DESIGN: CHENG

CHECKED:

AUDITED:

APPROVED:

DATE: 2010-06-14

DATE:

DATE:

DATE:

DWG. NO.

GRA71-Z002

PART NO.

GRA71-XX-XX-Z002

TITLE.

2.00 PIN HEADER
SINGLE ROW DIP TYPE
BASE 2.00H

