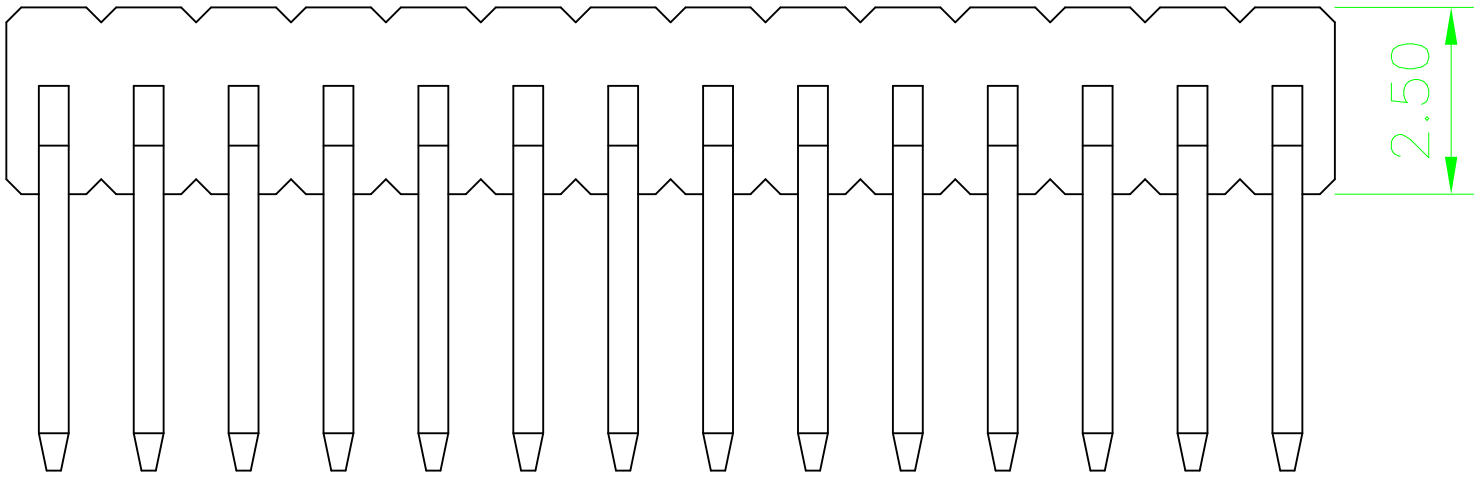
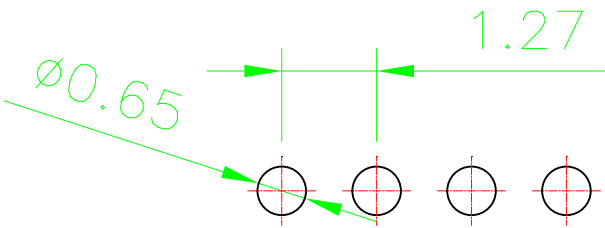


REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
01		INITIAL RELEASE.	2010.9.21	WU

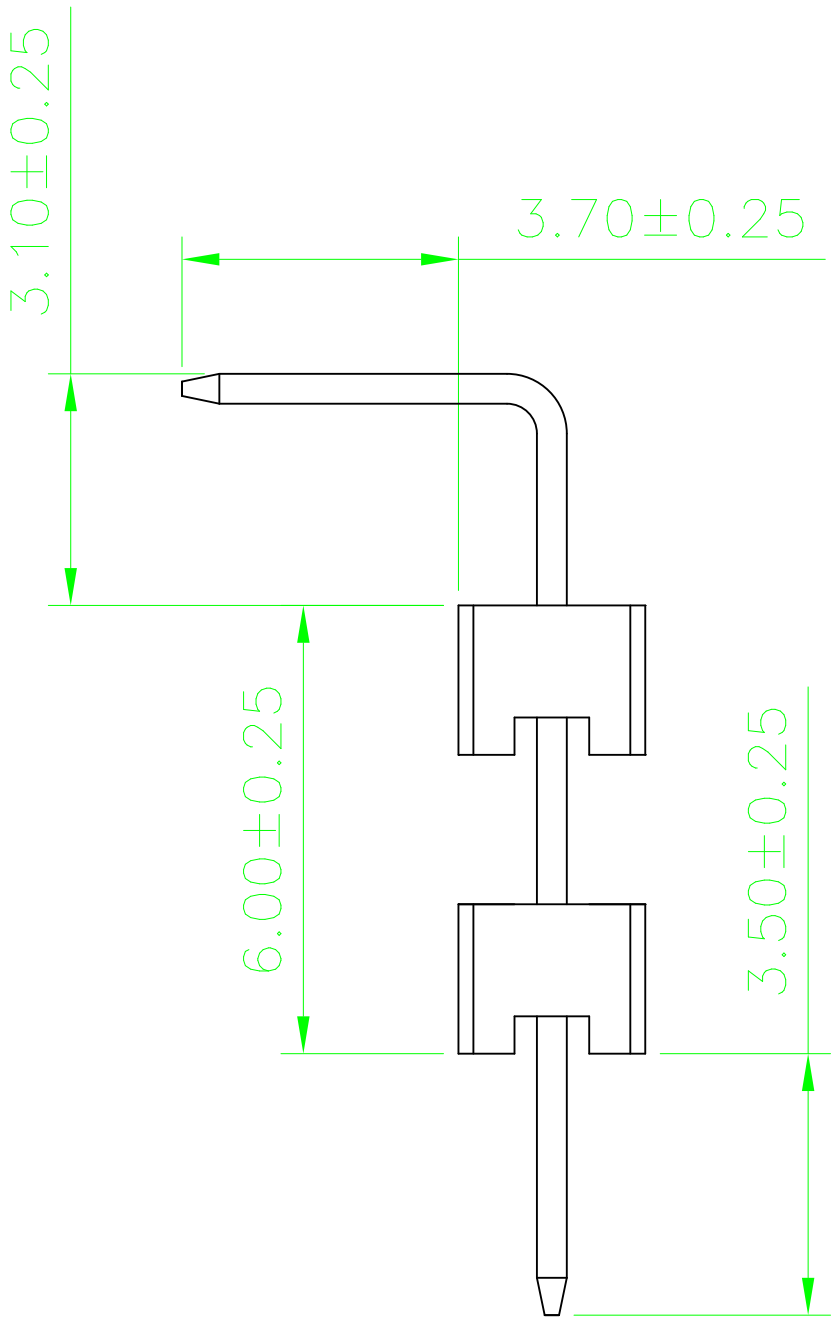
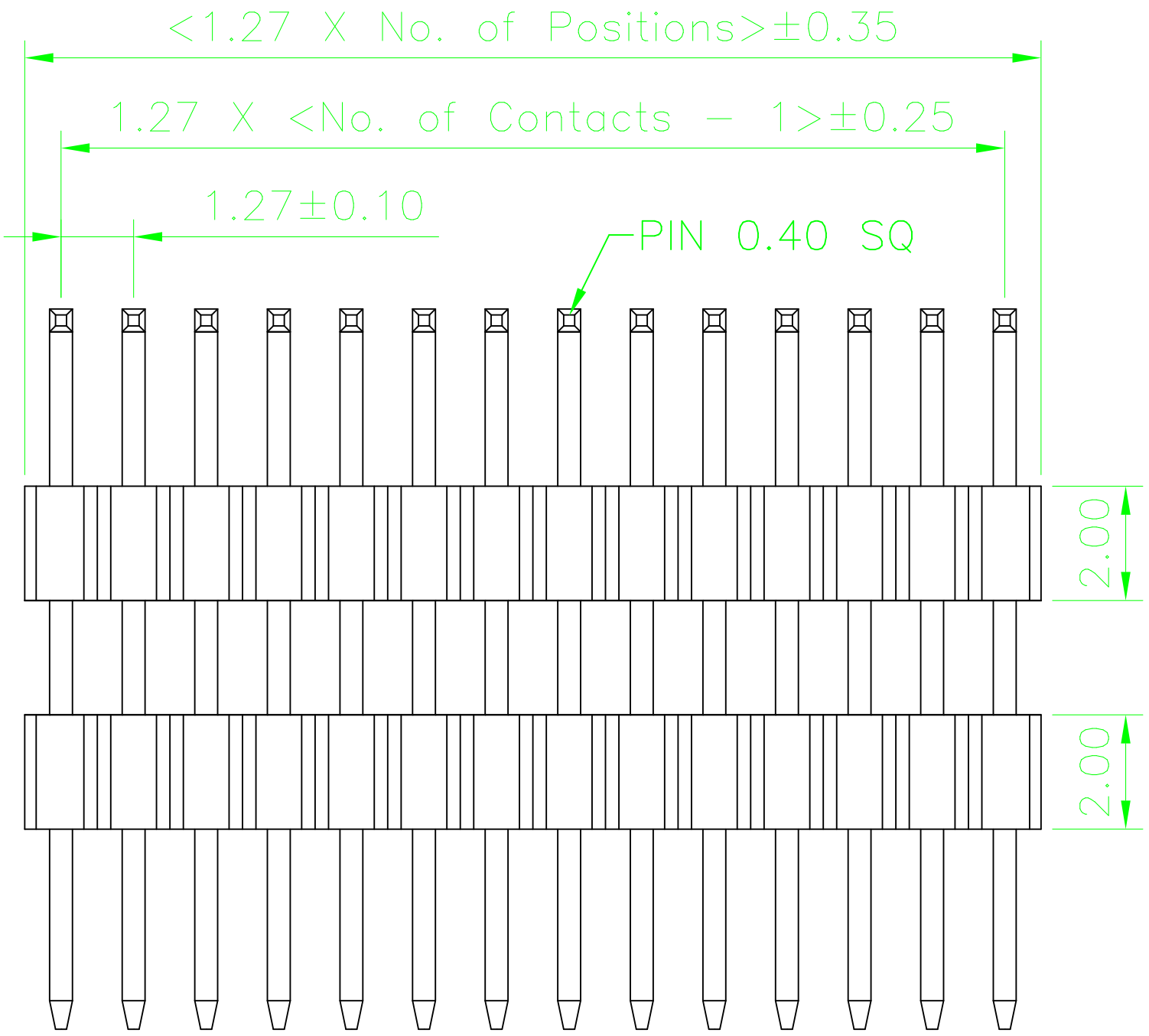


Recommended P.C.B.Layout
(Tolerance:±0.05)



Specifications
Current Rating: 1Amp
Insulation Resistance: 1000MΩ Min.
Dielectric Withstanding: 300V AC
Contact Resistance: 20mΩ Max.
Operating Temperature:−40℃ to +105℃

Materials
Contact Material: Phospor Bronze
Insulation Material: Polyester, UL 94V−0 LCP
All materials must meet Geronn requirement for environment protection



Ordering Information

GRA51 - XX XX W01 S2
No. of Pins per Row 02~33
Standard Plating
02=Gold Flash
Selective plating
01=Tin
05=Gold Flash/Tin

Geron Electronic Co.,Ltd

GENERAL TOLERANCE

X.° ± 5°	X. ± 0.30
.X° ± 3°	.X ± 0.25
.XX° ± 2°	.XX ± 0.15
.XXX° ± 1°	.XXX ± 0.10

DESIGN: ZHOU
CHECKED: WU
AUDITED: YANG
APPROVED: MEI

DATE: 2010.9.21
DATE: 2010.9.21
DATE: 2010.9.21
DATE: 2010.9.21

DWG. NO.
GRA51−W01−S2
PART NO.
GRA51−XX−XX−W01−S2

TITLE.
1.27 PIN HEADER
BOARD SPACER SINGLE
ROW DIP TYPE